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Karen Vertz	(Depositor's name)
Karen Vertz	(Signature)
1-5-06	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/686,011	10/14/2003	Luigi Colombo	TI-36145	5842

TITLE OF INVENTION: ENCAPSULATED MOS TRANSISTOR GATE STRUCTURES AND METHODS FOR MAKING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	$1400	$300	$1700	03/28/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
LINDSAY JR, WALTER LEE	2812	257-314000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Rose Alyssa Keagy
2 W. James Brady, III
3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(B) RESIDENCE: (CITY and STATE OR COUNTRY): Dallas, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent) : ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date 1/5/2006

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Luigi Colombo Docket Number: TI-36145

Serial No.: 10/686,011 Art Unit: 2812

Filed: 10/14/03 Examiner: Walter Lee Lindsay, Jr.

For: Encapsulated MOS Transistor Gate Structures and Methods for Making the Same

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

Karen Vertz — Karen Vertz

1-5-06 — Date

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___ FORMAL DRAWINGS	X ISSUE FEE & PUBLICATION FEE (1 Page)
___ INFORMAL DRAWINGS	___ REPLY BRIEF (IN TRIPLICATE) (# Pages)
___ CONTINUATION APP'N	___ ELECTION
___ DIVISIONAL APP'N	

NAME OF INVENTOR(S): Luigi Colombo	RECEIPT DATE & SERIAL NO.: **Serial No.: 10/686,011** **Filing Date:** 10/14/03
TITLE OF INVENTION: Encapsulated MOS Transistor Gate Structures and Methods for Making the Same	
TI FILE NO.: TI-36145 / DEPOSIT ACCT. NO.: 20-0668	
FAXED: 1-5-06 DUE: 3-28-06 ATTY/SEC'Y: RAK/kjv	

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